

What is claimed is:

1. A semiconductor integrated circuit device comprising:

a demodulation circuit for demodulating a reception signal;

a first oscillation circuit for generating a signal to be combined with the reception signal or an oscillation signal as a base of the signal;

a modulation circuit for modulating a transmission signal;

a second oscillation circuit for generating a signal to be combined with the transmission signal or an oscillation signal as a base of the signal;

a reference oscillation circuit for generating a reference frequency signal which determines the frequency of each of oscillation signals generated by said first and second oscillation circuits; and

first and second external terminals provided in correspondence with said reference oscillation circuit,

wherein said reference oscillation circuit includes a transistor of which control terminal is connected to said first external terminal, a capacitor element connected between said first and second external terminals, and a resistive element connected between said second external terminal and a power source voltage terminal, said reference oscillation circuit can oscillate when a part of an output of said transistor is

fed back to the control terminal of the transistor, and said second external terminal is set to a predetermined potential or an open state depending on either an oscillator or an electronic part for oscillation for outputting an oscillation signal is connected to said first external terminal.

2. The semiconductor integrated circuit device according to claim 1, wherein in said reference oscillation circuit, a part of an output of said transistor is fed back to the control terminal of the transistor via said capacitor element.

3. The semiconductor integrated circuit device according to claim 1, wherein said capacitor element is constructed by a first capacitor element and a second capacitor element connected in series between said first and second external terminals, an emitter terminal or a source terminal of said transistor is connected to a connection node between said first and second capacitor elements, and an emitter voltage or a source voltage of said transistor is fed back to the control terminal of the transistor via said first capacitor element.

4. The semiconductor integrated circuit device according to claim 1, further comprising a bias circuit for making current flow to said transistor.

5. The semiconductor integrated circuit device according to claim 4, wherein said bias circuit has a second transistor having a control terminal to which the voltage applied to the control terminal of said transistor is applied, a plurality of resistive elements connected in series with the second transistor, and a third transistor connected in series with said transistor, and a voltage divided by said plurality of resistive elements is applied to the control terminal of said third transistor, thereby determining the current made to flow to said transistor.

6. A wireless communication system comprising:
a semiconductor integrated circuit device according to claim 1; and
a second semiconductor integrated circuit device for processing a reception signal demodulated by said semiconductor integrated circuit device, generating a baseband signal on the basis of information to be transmitted, and supplying the baseband signal to said semiconductor integrated circuit device,
wherein an oscillator is connected to said first external terminal and said predetermined potential is set for said second external terminal.

7. The wireless communication system according to

claim 6, wherein a variable capacitor element is further connected in series with said oscillator between said first external terminal and a predetermined potential terminal on the outside, and a frequency control voltage output from said second semiconductor integrated circuit device is applied to a connection node between said oscillator and said variable capacitor element.

8. A wireless communication system comprising:

a semiconductor integrated circuit device according to claim 1; and

a second semiconductor integrated circuit device for processing a reception signal demodulated by said semiconductor integrated circuit device, generating a baseband signal on the basis of information to be transmitted, and supplying the baseband signal to said semiconductor integrated circuit device,

wherein an electronic part for oscillation is connected to said first external terminal, and the potential of said second external terminal is set in a floating state.

9. The wireless communication system according to claim 8, wherein said electronic part for oscillation has a control terminal, an oscillation frequency is made changeable in accordance with a signal input to the control terminal, and a frequency control signal output

from said second semiconductor integrated circuit device is input to the control terminal of said electronic part for oscillation.

10. The wireless communication system according to claim 6, wherein a signal used as an operation clock signal of an internal circuit of said second semiconductor integrated circuit device is supplied from said oscillation circuit to the second semiconductor integrated circuit device.

11. The wireless communication system according to claim 8, wherein a signal used as an operation clock signal of an internal circuit of said second semiconductor integrated circuit device is supplied from said oscillation circuit to the second semiconductor integrated circuit device.